

File No. 16519
Named Inventor: Leonard Forbes

Docket No.: 400.257US01

Title: **FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC**

Commissioner for Patents
 MS Patent Application
 P.O. Box 1450
 Alexandria, Virginia 22313-1450

 01004 U.S.PTO
 10/682590


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We are transmitting the following documents along with this Transmittal Sheet:

Utility Patent Application: Specification (14 pgs., including 22 claims and a 1 pg Abstract);
 5 Sheets of Formal drawings;
 A signed Declaration (3 pgs.);
 Power of Attorney by Assignee and Certificate by Assignee Under 37 CFR 3.73(b) (1 pg.);
 An Assignment of the invention to Micron Technology, Inc. (2 pgs.) and a Recordation Form Cover Sheet (1 pg.);
 A check in the amount of \$40.00 to cover the Assignment Recording Fee;
 A check in the amount of \$1,150.00 to cover the filing fee, which is calculated below;
 A return postcard.

CUSTOMER NUMBER 27073

APPLICATION FILING FEE					
	Number of Claims Filed (1)	Claims Included in Basic Filing Fee (2)	Number of Extra Claims (1-2)	Cost per Extra Claim	Fee Required
Total Claims	22	- 20 =	2	x \$18.00 =	\$ 36.00
Independent Claims	7	- 3 =	4	x \$86.00 =	\$ 344.00
One or More Multiple Dependent Claims Presented? If Yes, Enter \$290 Here *					\$ 0
Enter Basic Filing Fee (Utility Patent-\$770.00) Here *					\$ 770.00
Total Application Filing Fee					\$1,150.00

Please charge any additional fees or credit overpayment to Deposit Account No. 501373.

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"Express Mail" mailing label number: EL994236682US Date of Deposit: October 9, 2003
 These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to MS Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)